



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Glenn J. Leedy
 Application No. : 10/672,961 Confirmation No.: 9439
 Filed : September 26, 2003
 For : THREE DIMENSIONAL MULTI LAYER MEMORY AND
 CONTROL LOGIC INTEGRATED CIRCUIT
 STRUCTURE
 Group Art Unit : 2822
 Examiner : Monica Lewis

Mail Stop RCE
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97,
 applicant wishes to call the attention of the Examiner to the
 following documents:

| <u>U.S. Patent Documents</u> | | | |
|------------------------------|------------|------------------|-----|
| US 3,636,358 | 01/18/1972 | Groschwitz | (6) |
| US 3,932,932 | 01/20/1976 | Goodman | (2) |
| US 4,028,547 | 06/07/1977 | Eisenberger | (3) |
| US 4,393,127 | 07/12/1983 | Greschner et al. | (3) |
| US 4,528,072 | 07/09/1985 | Kurosawa et al. | (2) |
| US 4,566,037 | 01/21/1986 | Takatsu et al. | (6) |
| US 4,622,632 | 11/11/1986 | Tanimoto et al. | (6) |
| US 4,810,889 | 03/07/1989 | Yokomatsu et al. | (3) |
| US 5,051,326 | 09/24/1991 | Celler et al. | (3) |
| US 5,062,689 | 11/05/1991 | Koehler | (8) |
| US 5,117,282 | 05/26/1992 | Salatino | (7) |
| US 5,166,962 | 11/24/1992 | Murooka et al. | (3) |

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| US 5,169,805 | 12/08/1992 | Mok et al. | (4) |
| US 5,188,706 | 02/23/1993 | Hori et al. | (3) |
| US 5,245,277 | 09/14/1993 | Furtek et al. | (6) |
| US 5,283,107 | 02/01/1994 | Bayer et al. | (6) |
| US 5,293,457 | 03/08/1994 | Arima et al. | (6) |
| US 5,399,505 | 03/21/1995 | Dasse et al. | (6) |
| US 5,432,999 | 07/18/1995 | Capps et al. | (5) |
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| US 5,517,457 | 05/14/1996 | Sakui et al. | |
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| US 6,017,658 | 01/25/2000 | Rhee et al. | (9) |
| US 6,092,174 | 07/18/2000 | Roussakov | (6) |
| US 6,154,809 | 11/28/2000 | Ikenaga et al. | (6) |
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| JP 3127816 | 05/30/1991 | Canon KK |
| JP 3174715 | 07/29/1991 | Fujitsu Limited |
| WO 98/19337 | 05/07/1998 | Trusi Technologies, LLC |

Nonpatent Literature Documents

Wolf, Stanley and Richard N. Tauber; Silicon Processing For the VLSI Era, Volume 1: Process Technology; Sunset Beach, CA: Lattice Press, 1986, pages 191-194 (6)

European Search Report for Application No. EP 02 00 9643 (date completed: October 8, 2002)

Phys. Rev. B., Condens. Matter Mater. Phys. (USA), Physical Review B (Condensed Matter and Materials Physics), 15 March 2003, APS through AIP, USA.

S. Wolf, Silicon Processing For the VLSI Era, 1990, Lattice Press, Volume 2, page 191. (10)

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Svechnikov, S.V.; Kobyl'yatskaya, M.F.; (1)
Kimarskii, V.I.; Kaufman, A.P.; Kuzolev, Yu.
I.; Cherepov, Ye. I.; Fomin, B.I.; "A switching
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Wang, T.T.; "Low stress films of cyclized
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"Measurement of polyimide interlayer adhesion
using microfabricated structures"; 1988;
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Kochugova, I.V.; Nikolaeva, L.V.; Vakser, N.M., (1)
(M.I. Kalinin Leningrad Polytechnic Institute
(USSR); "Electrophysical investigation of thin-
layered inorganic coatings"; 1989; pp. 826-828.

The aforementioned references are listed on the accompanying Form PTO/SB/08 (submitted in duplicate). Pursuant to 37 C.F.R. 1.98 (a)(2), applicant is not submitting copies of the aforementioned U.S. patent document references. Copies of the aforementioned Foreign Patent Documents and Non-Patent Literature Documents are enclosed herewith.

Applicant reserves the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

The above-identified Non-Patent Literature Documents identified by (1) were previously cited in an Information Disclosure Statement filed on December 23, 2005, and were not considered by the Examiner. These references

are being re-submitted herewith for the Examiner's consideration.

The aforementioned documents identified by (2) were cited in an Office Action mailed on November 17, 2005 in co-pending commonly assigned U.S. Patent Application No. 10/742,057. Documents identified by (3) were cited in an Office Action mailed on December 7, 2005 in co-pending commonly assigned U.S. Patent Application No. 10/766,557. The Document identified by (4) was cited in an Office Action mailed on December 13, 2005 in co-pending commonly assigned U.S. Patent Application No. 10/742,282. The document identified by (5) was cited in an Office Action mailed on December 19, 2005 in co-pending commonly assigned U.S. Patent Application No. 10/379,820. The U.S. Patent Documents and Nonpatent Literature Documents identified by (6) were cited in an Office Action mailed on January 26, 2006 in co-pending commonly assigned U.S. Patent Application No. 10/741,647. The aforementioned document identified by (7) was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/742,057 on May 18, 2006; the document identified by (8) was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/385,386 on April 26, 2006; the document identified by (9) was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/766,557 on June 5, 2006; the document identified by (10) was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/379,820 on April 20, 2006.

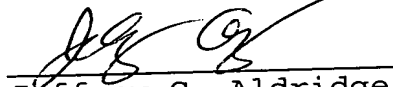
It is respectfully requested that these references be: (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed

on any patent which may issue on this application. Applicant requests that a copy of Form PTO-SB/08, as considered and initialed by the Examiner, be returned with the next communication.

This Statement is submitted with a Request for Continued Examination under 37 C.F.R. § 1.114. The Director is hereby authorized to charge \$180.00 in payment of the fee for submission of this Information Disclosure Statement pursuant to 37 C.F.R. §1.97(c)(2), payment of any additional fees required in connection with this Statement, or to credit any overpayment of the same, to Deposit Account No. 06-1075 (Order No.: 001202-0127). A duplicate copy of this Information Disclosure Statement is enclosed herewith.

An early and favorable action is respectfully requested.

Respectfully submitted,



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PTO/SB/08a/b (08-03)

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| Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | | | Complete if Known | |
| | | | | Application Number | 10/672,961 (Conf. No. 9439) |
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| | | | | Art Unit | 2822 |
| | | | | Examiner Name | Monica Lewis |
| Sheet | 1 | of | 2 | Attorney Docket Number | ELM-2 Cont. 4 |

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| | | Number-Kind Code ² (if known) | | | |
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| | | DE3233195 | 03-17-1983 | Mitsubishi Electric Corp. | | |
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|---------------------------------|--------------------------|---|----------------|--|
| Examiner Initials | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² | |
| | | Wolf, Stanley and Richard N. Tauber; Silicon Processing For the VLSI Era, Volume 1: Process Technology; Sunset Beach, CA: Lattice Press, 1986, pages 191-194 | | |
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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